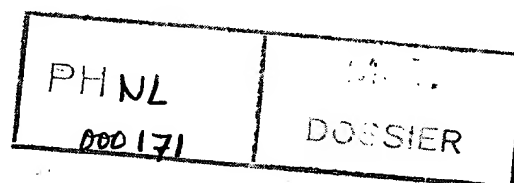


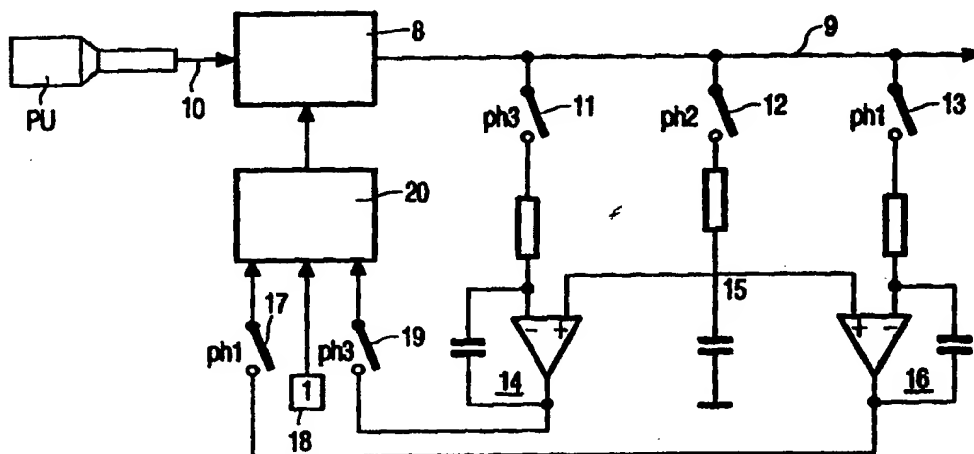


INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification 6 : H04N		A2	(11) International Publication Number: WO 99/56455
			(43) International Publication Date: 4 November 1999 (04.11.99)
(21) International Application Number: PCT/IB99/00641 (22) International Filing Date: 12 April 1999 (12.04.99) (30) Priority Data: 98201333.6 27 April 1998 (27.04.98) EP (71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL). (71) Applicant (for SE only): PHILIPS AB [SE/SE]; Kottbygatan 7, Kista, S-164 85 Stockholm (SE). (72) Inventors: VAN ROOY, Johannes, H., J., M.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). MOELANDS, Antonius, G.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). (74) Agent: STEENBEEK, Leonardus, J.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).			(81) Designated States: JP, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE). Published Without international search report and to be republished upon receipt of that report.



(54) Title: FLICKER COMPENSATION FOR CAMERAS



(57) Abstract

In a method of compensating an image signal (10) having a field frequency of substantially N times a mains frequency, in which $N \geq 2$, for AC light source induced fluctuations, the image signal (10) and an average signal representing an average image signal content over a given time period are arithmetically processed (14, 16-20, 8) to obtain a corrected signal (9), in which at least $N-1$ differences between at least $N-1$ respective phases of the corrected signal (9) on the one hand, and a selected phase (ph2) other than the $N-1$ phases of the corrected signal (9), or an average over N fields of the image signal (10), on the other hand, are integrated (14, 16), and in which at least $N-1$ integrated differences are processed (20) to obtain a correction factor for the image signal (10).

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

Flicker compensation for cameras.

The invention relates to flicker compensation for cameras, more specifically to flicker compensation for high-speed cameras such as slow-motion cameras operating at an increased field frequency.

5

JP-A-62/123,880 discloses a fluorescent lamp flicker correction circuit for a solid-state television camera. The magnitude of video signals is adjusted to lower flicker of a fluorescent lamp when a CCD element is used by adjusting the gain of a gain control circuit automatically according to the magnitude of video signals of each field. More specifically, video signals applied to an input terminal are applied to the gain control circuit and at the same time sent to a switching circuit. There, signals of one field are portioned to three signal lines including respective low-pass filters having the same characteristic to obtain three equalized signals which are switched successively by a switch. At the same time, they are equalized by an equalizing circuit. A divider divides the output from the switch by the output of the equalizing circuit to obtain gain control signals for the gain control circuit.

15

US-A-5,272,539 discloses another video camera with flicker cancellation. A flicker cancelling loop has a flicker detector and detects a flicker component level in the image pick-up signal and controls the gain of an AGC amplifier and/or an opening of an iris so that the flicker component level is cancelled.

20

US-A-5,293,238 discloses a television camera in which flicker which occurs when the television camera images an object under illumination of a flickering light source, such a fluorescent lamp, is minimized without causing sensitivity of a photo-electric conversion element and hence the camera to be degraded under a non-flickering light source. The television camera includes an automatic iris control device, an automatic gain control circuit, a micro-computer, a photo-electric conversion element, and an integration circuit for integrating an output signal of the photo-electric conversion element for each field period. The micro-computer sets a signal a signal storage time of the photo-electric conversion element to a value by which flicker noise can be restricted when flicker is detected on the basis of a change of an output signal of the integration circuit, and sets it to another value

25

when a change of light source is detected on the basis of a change of iris value and gain. The integration circuit functions to accumulate signal for every field period and to sample/hold the integrated signal. The determination of flicker can be done by storing the input signal from the integration circuit for at least 1 field period, sequentially comparing it with a stored
5 input signal for an immediately preceding field, and checking regularity.

The light output of artificial lighting from AC power sources is not constant in time. Especially FL and HMI light sources have a strong AC component. The cycle time of this effect is 1/100 sec for 50 Hz power sources and 1/120 sec for 60 Hz
10 power.

Modern CCD cameras have a controlled exposure time. To avoid beat frequencies in the exposed picture in artificial light situations the camera exposure time can be chosen to integrate exactly one cycle time of the artificial light source used.

The choice of 50 Hz field frequency in countries with 50 Hz mains supply and 60 Hz field frequency in countries with 60 Hz mains supply was also meant to avoid
15 beat frequencies between AC power and cameras. Even with exposure control switched to nominal, interference of the AC mains frequency with the field frequency only results in a beat frequency in the video signal at low frequencies ($f_{\text{mains}} - f_{\text{field}}$).

The above-mentioned prior art does not address the specific problems associated with slow-motion cameras operating at a high speed. For slow-motion applications
20 cameras are used with a field frequency higher than 50 or 60 Hz. Typically slow-motion cameras work at field frequencies N times higher than the broadcast field frequency, with $N=3$ as the most commonly used (150 or 180 fields/sec). The signal of such cameras can be recorded, and the sequence can be played back at the normal system speed of the broadcast system, resulting in a time expansion of N (=slow-motion).

25 The maximum exposure time of the camera is less than one cycle period of the artificial light, and each 2 periods of the light is sampled in N fields. This means that on light with strong AC content the occurrence of beat frequencies in the camera output signal will be unavoidable. The AC content of the light source is sampled in N phases, resulting in a repeating beat pattern over every N field periods. If the relationship between
30 field frequency and mains frequency is exact, a fixed cycle is the result, otherwise phase shifts will cause the pattern to change slowly, as the phase of the field sampling with regard to the light is changed. The disturbance of the signal then can be characterized as a pattern over N fields, slowly changing in time. Fig. 1 illustrates this. The horizontal axis represents time T in seconds, while the vertical axis represents the amplitude A. In Fig. 1, curve 1

represents the light output of a 50 Hz AC light source, while the rectangularly shaped curve 2 represents the average of curve 1 over one field period of a high speed camera having a field frequency of about 3×50 Hz. The most annoying effect is the fast change from field to field. The slow change in mean video level and field pattern is secondary.

5 Present slow-motion cameras have no means to correct for the effects described.

It is, *inter alia*, an object of the invention to provide improved slow-
10 motion cameras. To this end, a first aspect of the invention provides a method and a device as defined by claims 1 and 3. A second aspect of the invention provides a slow-motion camera as defined by claim 4. An advantageous embodiment is defined in dependent claim 2.

A primary aspect of the invention provides a method of compensating an image signal having a field frequency of substantially N times a mains frequency, in which N
15 ≥ 2 , for AC light source induced fluctuations, the image signal and an average signal representing an average image signal content over a given time period are arithmetically processed to obtain a corrected signal, in which at least N-1 differences between at least N-1 respective phases of the corrected signal on the one hand, and a selected phase (ph2) other than the N-1 phases of the corrected signal, or an average over N fields of the image signal,
20 on the other hand, are integrated, and in which at least N-1 integrated differences are processed to obtain a correction factor for the image signal.

These and other aspects of the invention will be apparent from and elucidated with reference to the embodiments described hereinafter.

25

In the drawings:

Fig. 1 illustrates the effects of an AC light source for a slow-motion camera;

Fig. 2 shows a camera comprising a first embodiment of a correction
30 circuit in accordance with the present invention; and

Fig. 3 shows a second embodiment of a correction circuit in accordance with the present invention.

To correct the fast flicker effect in the camera the following observations can be made:

- a) The average scene content of the scene from field to field does not change much in a short time period, apart from effects from lighting that we want to compensate.
- 5 b) When the field frequency of the camera is close to N times the lighting frequency a constant or slowly changing nearly repeating beat pattern over N fields can be observed.
- c) Fast changes in light can be compensated in the camera with gain in the camera or exposure time changes of the sensor.
- 10 d) Red, green and blue phosphors in light sources can have different time constants, so that the behavior in R,G and B in the camera can be different, resulting in colored flicker effects.

Observations a) and b) lead to the conclusion that an adaptive compensation system acting on the average video in at least N fields can be used to
15 compensate for flicker. Observations c) and d) imply that separate corrections for red, green and blue are necessary, either by separately controlling the gain or by separately controlling exposure time of the sensors.

For clarity, the situation for $N=3$ is given, but this does not limit the application.

20 Fig. 2 gives an embodiment of a camera including an analog realisation of a feedback type compensation. While the compensation path of only one color signal is shown, a complete camera will include three of such compensation paths, one for each color. The incoming color signal 10 from a pick-up unit PU is gain compensated by the signal multiplier 8. After the signal multiplier 8 the average color signal level of one (ph2) of the N
25 phases ph1, ph2, ph3 is sampled by a switch 12 and a capacitor 15. For this phase ph2 the gain is set to one (18). N-1 control loops vary the gain for the other N-1 phases ph1, ph3 to get the same average color signal level in these phases. Each loop includes a switch 11,13, and an integrator 14,16 which integrates the difference between the average color signal level of that particular phase to the result of the reference phase stored by the capacitor 15. The
30 combination of switches 11 and 19, and 13 and 17 switches the control signal per phase ph1, ph3 into the multiplier 8. The multiplier gain is the sum (calculated by an adder 20) of unity gain (18) and the output of integrator 16 if phase ph1 is processed, unity if phase ph2 is processed, and the sum of unity and the output of integrator 14 if phase ph3 is processed.

This processing uses no dividers (complex circuits, and very sensitive to

offsets), and can be easily accomplished in analog hardware.

As one (ph2) of the N phases ph1-ph3 is used as a reference, and the N-1 others are compensated to give the same average color signal level, there is a disadvantage: the slow color signal changes when phase shifts occur, are not compensated, and for the relatively short temporal integration of only one field they can be quite strong.

Fig. 3 gives another realisation using the average of N fields as a reference, to minimise the slow level changes. Basically the situation is the same as in the previous realisation, but the reference signal for the integrators is derived as a moving average from N fields before the gain multiplier. The longer exposure time of this average signal, which spans more than one light cycle, diminishes the slow beat frequency in the corrected signal in comparison to the previous realisation. An averager 34 obtains the average AVG123 over N fields of the input color signal 1 received thru N switches 31, 32, 33. In Fig. 4, all N phases of the color output signal 9 are integrated by integrators 24, 25, 26 which receive the color output signal 9 thru switches 21, 22, 23. The average AVG123 is input as a reference signal to all N integrators 24, 25, 26. Output signals of the N integrators 24, 25, 26 are applied to an adder 30 thru N switches 27, 28, 29, to obtain the control signal for the gain multiplier 8.

The required number N of phases in the control depends on the light frequency fL and the field frequency fF of the high-speed camera as follows:

$$N = fF / \text{GCF}(fL, fF),$$

in which GCF(fL, fF) is the greatest common divisor of fL and fF, and the light frequency fL is usually twice the mains frequency. The table below gives some examples.

	fF	fL	GCD(fF, fL)	N	application example
25	300	120	60	5	300 Hz (5*60) slow-motion camera in 60 Hz country
	300	100	100	3	300 Hz (5*60) slow-motion camera in 50 Hz country
	250	120	10	12	250 Hz (5*50) slow-motion camera in 60 Hz country
	250	100	50	5	250 Hz (5*50) slow-motion camera in 50 Hz country
	180	120	60	3	180 Hz (3*60) slow-motion camera in 60 Hz country
30	180	100	20	9	180 Hz (3*60) slow-motion camera in 50 Hz country
	150	120	30	5	150 Hz (3*50) slow-motion camera in 60 Hz country
	150	100	50	3	150 Hz (3*50) slow-motion camera in 50 Hz country

Although the examples of realisation are rather specific digital or analog,

compensations can be implemented as analog circuits, as digital circuits or algorithms in a microprocessor or digital signal processor (DSP). In the examples controlled gain is used to compensate the flicker, control of exposure time can also be used. As exposure time can only be shortened with regard to the nominal situation, the penalty is a decrease in sensitivity of the camera. When exposure time is used as a control, the gain circuit has to be replaced with a controlled timing device to individually control the start of the light integration of each of N fields within the sensor. From the examples in the table above it appears that the invention can even be applied in a slow-motion camera intended for the 50 Hz (or 60 Hz) market but used in a 60 Hz (or 50 Hz) country. That application is included within the scope of the claims.

A primary aspect of the invention can be summarized as follows. A video camera operating at a field frequency near a multiple N of the mains frequency with a control circuit to compensate for field flicker from lighting conditions, in which a compensation is derived from the average video content per field, and either N control loops to compensate individual fields in a cycle of N to an average video level, or N-1 loops to compensate N-1 fields to the average video level of the Nth field, in which $N = 3, 4, \text{etc.}$

It should be noted that the above-mentioned embodiments illustrate rather than limit the invention, and that those skilled in the art will be able to design many alternative embodiments without departing from the scope of the appended claims. In the claims, any reference signs placed between parentheses shall not be construed as limiting the claim. The word "comprising" does not exclude the presence of other elements than those listed in a claim. The invention can be implemented by means of hardware comprising several distinct elements, and by means of a suitably programmed computer. In the device claim enumerating several means, several of these means can be embodied by one and the same item of hardware.

Claims:

1. A method of compensating an image signal (10) having a field frequency of substantially N times a mains frequency, in which $N \geq 2$, for AC light source induced fluctuations, the method comprising the steps of:

receiving said image signal (10) having said field frequency of substantially N times a mains frequency and being distorted by said AC light source induced fluctuations;

generating (15; 34) an average signal representing an average image signal content over a given time period; and

arithmetically processing (14,16-20,8; 24-30,8) said image signal (10) and said average signal to obtain a corrected signal (9), wherein said arithmetically processing step includes the steps of:

integrating (14,16; 24-26) at least N-1 differences between at least N-1 respective phases of said corrected signal (9) on the one hand, and a selected phase (ph2) other than said N-1 phases of said corrected signal (9), or an average (AVG123) over N fields of said image signal (10), on the other hand; and

processing (20; 30) at least N-1 integrated differences to obtain a correction factor for said image signal (10).

2. A method as claimed in claim 1, wherein said field frequency is substantially N times the mains frequency, in which $N \geq 2$, and said arithmetically processing step includes the step of integrating (24-26) N differences between N respective phases of said corrected signal (9) and an average (AVG123) over N fields of said image signal (10).

3. A device for compensating an image signal (10) having a field frequency of substantially N times a mains frequency, in which $N \geq 2$, for AC light source induced fluctuations, the device comprising:

means for receiving said image signal (10) having said field frequency of substantially N times a mains frequency and being distorted by said AC light source induced fluctuations;

means for generating (15; 34) an average signal representing an average

image signal content over a given time period; and

means for arithmetically processing (14,16-20,8; 24-30,8) said image signal (10) and said average signal to obtain a corrected signal (9), wherein said arithmetically processing means include

5 means for integrating (14,16; 24-26) at least N-1 differences between at least N-1 respective phases of said corrected signal (9) on the one hand, and a selected phase (ph2) other than said N-1 phases of said corrected signal (9), or an average (AVG123) over N fields of said image signal (10), on the other hand; and

means for processing (20; 30) at least N-1 integrated differences to obtain
10 a correction factor for said image signal (10).

4. A slow-motion camera, comprising:

an image signal pick-up unit (PU) operated at a field frequency of substantially N times a mains frequency, in which $N \geq 2$, for generating an image signal (10); and

15 a device for compensating said image signal (10) for AC light source induced fluctuations, as defined by claim 3.

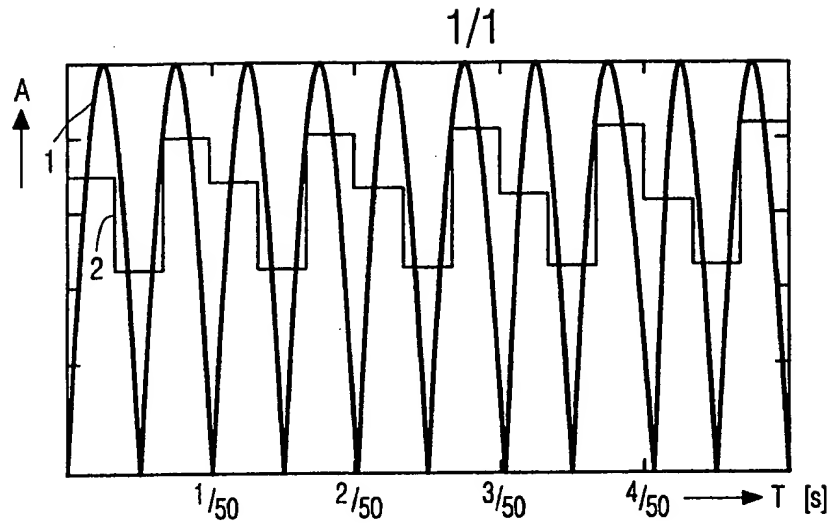


FIG. 1

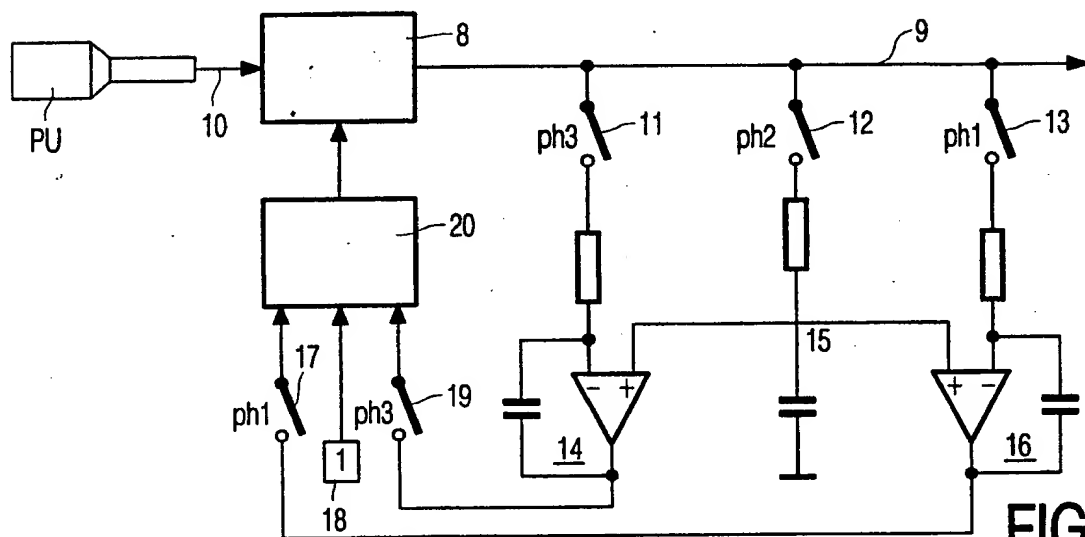


FIG. 2

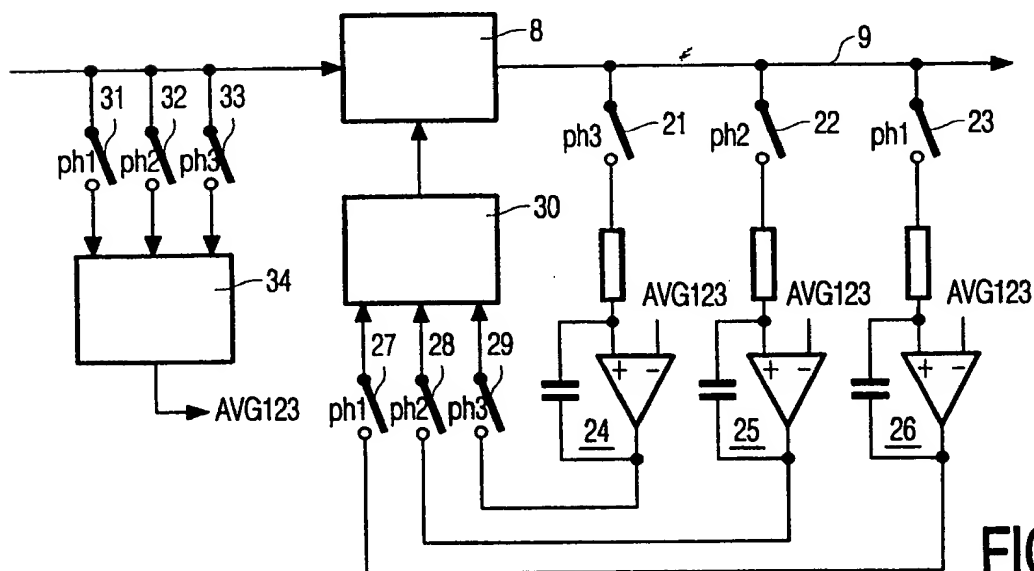


FIG. 3

THIS PAGE BLANK (USPTO)

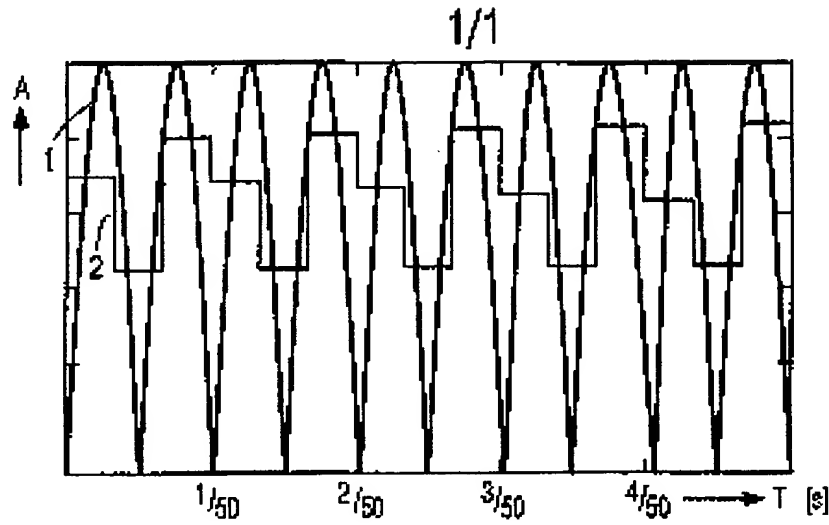
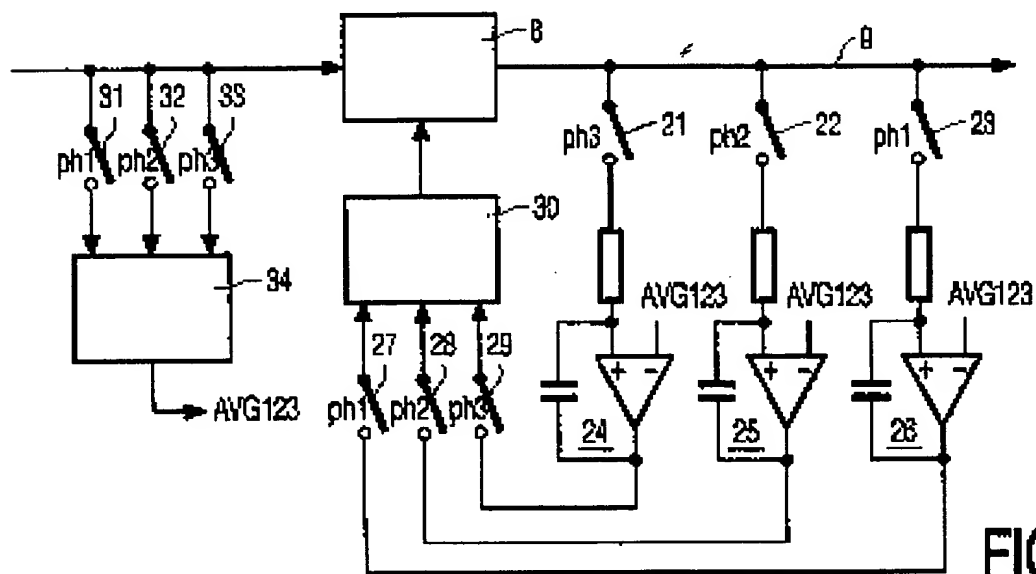
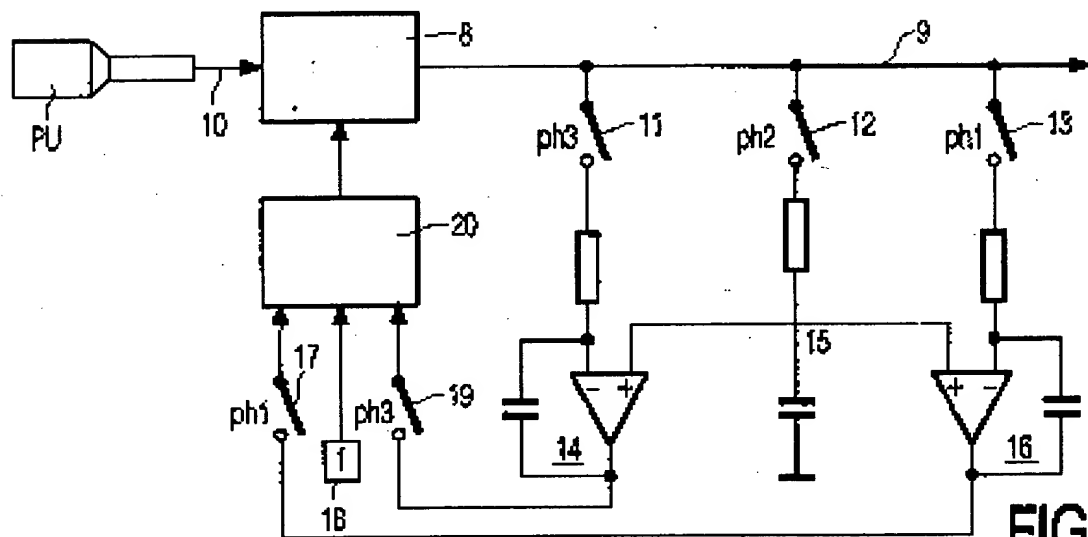


FIG. 1



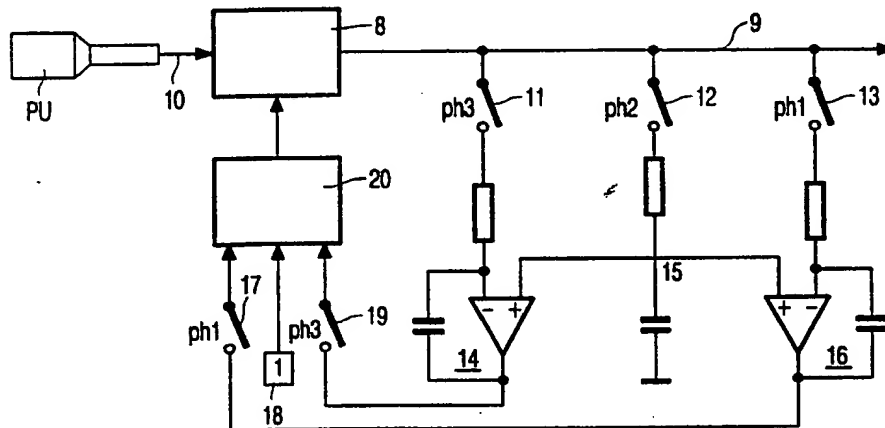
THIS PAGE BLANK (USPTO)



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶ : H04N 5/235, 5/44		A3	(11) International Publication Number: WO 99/56455
			(43) International Publication Date: 4 November 1999 (04.11.99)
(21) International Application Number: PCT/IB99/00641		(81) Designated States: JP, European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE).	
(22) International Filing Date: 12 April 1999 (12.04.99)			
(30) Priority Data: 98201333.6 27 April 1998 (27.04.98) EP		Published With international search report. Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.	
(71) Applicant: KONINKLIJKE PHILIPS ELECTRONICS N.V. [NL/NL]; Groenewoudseweg 1, NL-5621 BA Eindhoven (NL).		(88) Date of publication of the international search report: 6 January 2000 (06.01.00)	
(71) Applicant (for SE only): PHILIPS AB [SE/SE]; Kottbygatan 7, Kista, S-164 85 Stockholm (SE).			
(72) Inventors: VAN ROOY, Johannes, H., J., M.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL). MOELANDS, Antonius, G.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).			
(74) Agent: STEENBEEK, Leonardus, J.; Prof. Holstlaan 6, NL-5656 AA Eindhoven (NL).			

(54) Title: FLICKER COMPENSATION FOR CAMERAS



(57) Abstract

In a method of compensating an image signal (10) having a field frequency of substantially N times a mains frequency, in which $N \geq 2$, for AC light source induced fluctuations, the image signal (10) and an average signal representing an average image signal content over a given time period are arithmetically processed (14, 16-20, 8) to obtain a corrected signal (9), in which at least $N-1$ differences between at least $N-1$ respective phases of the corrected signal (9) on the one hand, and a selected phase (ph2) other than the $N-1$ phases of the corrected signal (9), or an average over N fields of the image signal (10), on the other hand, are integrated (14, 16), and in which at least $N-1$ integrated differences are processed (20) to obtain a correction factor for the image signal (10).

FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

INTERNATIONAL SEARCH REPORT

International application No.
PCT/IB 99/00641

A. CLASSIFICATION OF SUBJECT MATTER

IPC6: H04N 5/235, H04N 5/44

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC6: H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document; with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5293238 A (TAKAHIRO NAKANO ET AL), 8 March 1994 (08.03.94), abstract --	1-4
A	US 5701526 A (HIROYUKI IWASAKI), 23 December 1997 (23.12.97), abstract --	1-4
A	US 5515132 A (HIROYUKI IWASAKI), 7 May 1996 (07.05.96), abstract -----	1-4

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"I" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"I" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

18 November 1999

Name and mailing address of the ISA:

Swedish Patent Office
Box 5055, S-102 42 STOCKHOLM
Facsimile No. +46 8 666 02 86

Date of mailing of the international search report

22-11-1999

Authorized officer

Michel Gascoin/mj

Telephone No. +46 8 782 25 00

INTERNATIONAL SEARCH REPORT
Information on patent family members

International application No.
PCT/IB 99/00641

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
US 5293238 A	08/03/94	JP 4373365 A KR 9509665 B	25/12/92 25/08/95
US 5701526 A	23/12/97	JP 9160087 A	20/06/97
US 5515132 A	07/05/96	EP 0588438 A JP 6095201 A	23/03/94 08/04/94